

**IEEE KITCHENER-WATERLOO**  
**IEEE MTT-Chapter Presentation**

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**"Materials Design and Integration"**

**Abstract:**

Organic/polymer thin film transistors (TFTs) are potentially useful for fabricating very low-cost integrated circuits for large-area electronic device (e.g., active matrix displays) where use of current silicon technology can be prohibitively costly. Their compatibility with flexible plastic substrates has also rendered them ideal for creating compact, lightweight, mechanically flexible, and structurally inspiring electronic device designs (e.g. electronic papers). For low-cost manufacturing, fabrication using simple solution-based techniques such as printing in a non-sterile environment is highly desirable. This presentation will discuss the issues, challenges, and advances in the development of enabling materials for fabricating low-cost TFTs. Our progress in the design of high-performing semiconductor polymers and other TFT materials will be presented.

**DATE: Monday May 3, 2004**

**TIME: 10:30 am**

**LOCATION: DC 1302, University of Waterloo Davis Centre**

**Invited by Prof. Arokia Nathan**  
Electrical & Computer Engineering Department

**ALL ARE WELCOME, REFRESHMENTS WILL BE SERVED**