

EDUCATION

2005-2010 **Stanford University**

Ph. D. in Electrical Engineering (2010) (Advisor: Prof. H. –S. Philip Wong)

- Thesis: *Nanoelectronics—Technology Assessment and Projection at the Device, Circuit, and System Level*

M. S. in Electrical Engineering (2007)

2001-2005 **Peking University**

B. S. in Microelectronics, B. S. in Economics

SELECTED SERVICES AND GRANTS

- **Executive Committee Member, IEEE Electron Device Society, Santa Clara Valley Chapter, 2013**
 - **Key Contributor to the International Technology Roadmap of Semiconductor, 2009 Edition, 2009**
 - **Technical Program Committee Member of the following academic conferences**
 - *GLSVLSI* (Great lake Symposium on VLSI), 2017; *ISLPED* (the International Symposium on Low Power Electronics and Design), 2013; *VLSI-TSA* (the International Symposium on VLSI Technology, Systems and Applications), 2013-2017; *IEDM* (the International Electron Devices Meeting, the premier conference in the device area), 2011-2012
 - **Reviewer for journals (selected)**
 - Proceedings of *IEEE*; *IEEE Transactions on Electron Devices (TED)*; *IEEE Electron Device Letters (EDL)*; *IEEE Transactions on Computer-Aided Design (TCAD)*; *IEEE Transactions on Circuits and Systems I (TCAS-I)*; *IEEE Transactions on Very Large Scale Integrated Circuits (TVLSI)*
(Named Golden Reviewer for *TED* in 2010-2016 and for *EDL* in 2010/2012/2013.)
 - **Recipient of the following grants (selected)**
 - **NSERC: Discovery** (L. Wei, 2014); **NSERC Strategic** (S. Boumaiza and L. Wei, 2015); **NSERC RTI Category I** (L. Wei *et al*, 2015); **CFI-JELF** (L. Wei *et al*, 2016); **ORF-RI** (L. Wei *et al*, 2016)
 - **Multiple grants from University of Waterloo, International Research Program Grant (IRPG)**
 - **Multiple grants from Waterloo Institute for Sustainable Energy (WISE)**
- * Total awarded grants as PI: CAD\$ 649, 000. Total awarded grants as co-PI: CAD\$ 506,617.

RESEARCH EXPERIENCE

2014-present **Assistant Professor, Faculty of Engineering, University of Waterloo, Waterloo, Canada**

- Nonvolatile memory based error resilient circuit design
- Device-circuit interactive design and optimization
- Low-dimensional material based integrated circuit for energy efficient applications and error resilient computing
- Gallium-Nitride devices for energy-efficient millimeter-wave (mm)/radio frequency (RF) applications
- Self-powered and low-power integrated system design for energy harvesting and smart grid monitoring applications

2012-2014 **Member of Technical Staff, Altera Corporation, San Jose, USA**

2010-2012 **Postdoctoral Associate, Massachusetts Institute of Technology, Cambridge, USA**

2005-2010 **Research Assistant, Stanford University, Stanford, USA**

TEACHING AND SUPERVISION EXPERIENCE

Courses

- ECE730 Topic 30 Advanced VLSI Devices, University of Waterloo (Graduate course)
- ECE140 Linear Circuit, University of Waterloo (1st year ECE core course)
- ECE240 Electronic Circuit I, University of Waterloo (2nd year ECE core course)
- ECE445 Integrated Digital Electronics, University of Waterloo (4th year ECE elective course)

Supervision

- Ph. D students: current – 2
- Master students: current – 3, graduated – 3
- Postdoc: past – 1

PUBLICATIONS

- Refereed International Journal Publications: 13 (11 with major contributions)
- Referred International Conference Publication: 35 (25 with major contributions)
- Seminars and Invited Talks: 22