REF: Resource Elasticity Fairness with Sharing Incentives for Multiprocessors

Seyed Majid Zahedi, Benjamin C. Lee zahedi@cs.duke.edu, benjamin.c.lee@duke.edu







• Alvy and Ben are working on ASPLOS papers







- Alvy and Ben are working on ASPLOS papers
- Each has \$10K to buy clusters







- Alvy and Ben are working on ASPLOS papers
- Each has \$10K to buy clusters
- Alvy works on accelerators







- Alvy and Ben are working on ASPLOS papers
- Each has \$10K to buy clusters
- Alvy works on accelerators
- Ben works on memory systems



• Alvy and Ben are strategic



- Alvy and Ben are strategic
- Which is better?
 - Small, separate clusters
 - Large, shared cluster





- Alvy and Ben are strategic
- Which is better?
 - Small, separate clusters
 - Large, shared cluster
- Suppose Alvy and Ben share





- Alvy and Ben are strategic
- Which is better?
 - Small, separate clusters
 - Large, shared cluster
- Suppose Alvy and Ben share
 - Is allocation fair?





- Alvy and Ben are strategic
- Which is better?
 - Small, separate clusters
 - Large, shared cluster
- Suppose Alvy and Ben share
 - Is allocation fair?
 - Is lying beneficial?





• Users must share



- Users must share
 - Overlooks strategic behavior



- Users must share
 - Overlooks strategic behavior
- Fairness policy is equal slowdown



- Users must share
 - Overlooks strategic behavior
- Fairness policy is equal slowdown
 - Fails to encourage envious users to share



- Users must share
 - Overlooks strategic behavior
- Fairness policy is equal slowdown
 - Fails to encourage envious users to share
- Heuristic mechanisms enforce equal slowdown



- Users must share
 - Overlooks strategic behavior
- Fairness policy is equal slowdown
 - Fails to encourage envious users to share
- Heuristic mechanisms enforce equal slowdown
 - Fail to give provable guarantees





- Equity
 - Evaluating others' and own position on equal terms



- Equity
 - Evaluating others' and own position on equal terms
 - No user envies another's allocation (i.e., envy-freeness)



- Equity
 - Evaluating others' and own position on equal terms
 - No user envies another's allocation (i.e., envy-freeness)
- Pareto Efficiency
 - No other allocation improves utility without harming others



REF is an allocation mechanism that guarantees game-theoretic desiderata for shared chip multiprocessors



REF is an allocation mechanism that guarantees game-theoretic desiderata for shared chip multiprocessors

• Envy-Free (EF)

No user envies another's allocation



REF is an allocation mechanism that guarantees game-theoretic desiderata for shared chip multiprocessors

• Envy-Free (EF)

No user envies another's allocation

• Pareto-Efficient (PE)

No other allocation improves utility without harming others



REF is an allocation mechanism that guarantees game-theoretic desiderata for shared chip multiprocessors

• Envy-Free (EF)

No user envies another's allocation

• Pareto-Efficient (PE)

No other allocation improves utility without harming others

• Sharing Incentives (SI)

Users perform no worse than under equal division



REF is an allocation mechanism that guarantees game-theoretic desiderata for shared chip multiprocessors

• Envy-Free (EF)

No user envies another's allocation

• Pareto-Efficient (PE)

No other allocation improves utility without harming others

• Sharing Incentives (SI)

Users perform no worse than under equal division

• Strategy-Proof (SP)

No user benefits from lying



Three Main Steps

• Defining utility functions



Three Main Steps

- Defining utility functions
- Identifying conditions for fairness



Three Main Steps

- Defining utility functions
- Identifying conditions for fairness
- Devising mechanism for finding allocations



• Model diminishing marginal returns (DMR)



- Model diminishing marginal returns (DMR)
 - Data locality affects returns from cache sizing



- Model diminishing marginal returns (DMR)
 - Data locality affects returns from cache sizing
 - Memory intensity affects returns from bandwidth



- Model diminishing marginal returns (DMR)
 - Data locality affects returns from cache sizing
 - Memory intensity affects returns from bandwidth
 - Serial portion affects returns from cores



- Model diminishing marginal returns (DMR)
 - Data locality affects returns from cache sizing
 - Memory intensity affects returns from bandwidth
 - Serial portion affects returns from cores
- Model substitution effects



- Model diminishing marginal returns (DMR)
 - Data locality affects returns from cache sizing
 - Memory intensity affects returns from bandwidth
 - Serial portion affects returns from cores
- Model substitution effects
 - Complementary resources can be traded



- Model diminishing marginal returns (DMR)
 - Data locality affects returns from cache sizing
 - Memory intensity affects returns from bandwidth
 - Serial portion affects returns from cores
- Model substitution effects
 - Complementary resources can be traded
 - E.g., cache size and memory bandwidth



Cobb-Douglas Utility

$$\mathsf{u}(\mathsf{x}) = \prod_{\mathsf{r}=1}^{\mathsf{R}} \mathsf{x}_{\mathsf{r}}^{lpha_{\mathsf{r}}}$$

u utility (e.g., performance)


$$\mathsf{u}(\mathsf{x}) = \prod_{\mathsf{r}=1}^{\mathsf{R}} \mathsf{x}_{\mathsf{r}}^{\alpha_{\mathsf{r}}}$$

- **u** utility (e.g., performance)
- $\boldsymbol{x}_{\boldsymbol{r}}$ allocation for resource \boldsymbol{r}



$$\mathsf{u}(\mathsf{x}) = \prod_{\mathsf{r}=1}^{\mathsf{R}} \mathsf{x}_{\mathsf{r}}^{\alpha_{\mathsf{r}}}$$

- **u** utility (e.g., performance)
- $\boldsymbol{x_r}$ allocation for resource \boldsymbol{r}
- $\alpha_{\mathbf{r}}$ elasticity for resource \mathbf{r}



$$\mathsf{u}(\mathsf{x}) = \prod_{\mathsf{r}=1}^{\mathsf{R}} \mathsf{x}_{\mathsf{r}}^{\alpha_{\mathsf{r}}}$$

- **u** utility (e.g., performance)
- $\mathbf{x}_{\mathbf{r}}$ allocation for resource \mathbf{r}
- $\alpha_{\mathbf{r}}$ elasticity for resource \mathbf{r}
- Cobb-Douglas fits preferences in computer architecture



$$\mathsf{u}(\mathsf{x}) = \prod_{\mathsf{r}=1}^{\mathsf{R}} \mathsf{x}_{\mathsf{r}}^{\alpha_{\mathsf{r}}}$$

- **u** utility (e.g., performance)
- $\boldsymbol{x_r}$ allocation for resource \boldsymbol{r}
- $\alpha_{\mathbf{r}}$ elasticity for resource \mathbf{r}
- Cobb-Douglas fits preferences in computer architecture
- Exponents introduce non-linearity which captures DMR



$$\mathsf{u}(\mathsf{x}) = \prod_{\mathsf{r}=1}^{\mathsf{R}} \mathsf{x}_{\mathsf{r}}^{\alpha_{\mathsf{r}}}$$

- **u** utility (e.g., performance)
- $\boldsymbol{x_r}$ allocation for resource \boldsymbol{r}
- $\alpha_{\mathbf{r}}$ elasticity for resource \mathbf{r}
- Cobb-Douglas fits preferences in computer architecture
- Exponents introduce non-linearity which captures DMR
- Products model substitution effects



$$\mathsf{u}_1 = \mathsf{x}_1^{0.6} \mathsf{y}_1^{0.4} \qquad \mathsf{u}_2 = \mathsf{x}_2^{0.2} \mathsf{y}_2^{0.8}$$



$$u_1 = x_1^{0.6} y_1^{0.4}$$
 $u_2 = x_2^{0.2} y_2^{0.8}$

u_1, u_2 utilities derived from performance measurements



$$\mathsf{u}_1 = \mathsf{x}_1^{0.6} \mathsf{y}_1^{0.4} \qquad \mathsf{u}_2 = \mathsf{x}_2^{0.2} \mathsf{y}_2^{0.8}$$

 u_1, u_2 utilities derived from performance measurements x_1, x_2 allocated memory bandwidth for users 1, 2



10/28

$$\mathsf{u}_1 = \mathsf{x}_1^{0.6} \mathsf{y}_1^{0.4} \qquad \mathsf{u}_2 = \mathsf{x}_2^{0.2} \mathsf{y}_2^{0.8}$$

- u_1, u_2 utilities derived from performance measurements
- x_1, x_2 allocated memory bandwidth for users 1, 2
- y_1, y_2 allocated cache size for users 1, 2



Three Main Steps

- Defining utility functions
- Identifying conditions for fairness
- Devising mechanism for finding allocations



Possible Allocations

- 2 users
- 12MB cache
- 24GB/s bandwidth





Envy-Free (EF) Allocations

- Identify EF allocations for each user
- $u_1(A_1) \ge u_1(A_2)$
- $\bullet \ u_2(A_2) \geq u_2(A_1)$



Envy-Free (EF) Allocations

- Identify EF allocations for each user
- $u_1(A_1) \ge u_1(A_2)$
- $\bullet \ u_2(A_2) \geq u_2(A_1)$





No other allocation improves utility without harming others



No other allocation improves utility without harming others



• Indifference curve: allocations that give same utility



No other allocation improves utility without harming others



• Indifference curve: allocations that give same utility



No other allocation improves utility without harming others



- Indifference curve: allocations that give same utility
- Contract curve: all Pareto-efficient allocations



Fair Allocations

 ${\sf Fairness} = {\sf envy-freeness} + {\sf Pareto-efficiency}$



Fair Allocations

Fairness = envy-freeness + Pareto-efficiency



Memory Bandwidth



Fair Allocations

Fairness = envy-freeness + Pareto-efficiency



Many possible fair allocations!



Three Main Steps

- Defining utility functions
- Identifying conditions for fairness
- Devising mechanism for finding allocations



Resource Elasticity Fairness Mechanism



- REF: fair allocation mechanism
- Guarantees desiderata
 - Sharing incentives
 - Envy-freeness
 - Pareto-efficiency
 - Strategy-proofness in large



Profiling for REF



- Off-line profiling
 - Synthetic benchmarks



Profiling for REF



- Off-line profiling
 - Synthetic benchmarks
- Off-line simulations
 - Various hardware



Profiling for REF



- Off-line profiling
 - Synthetic benchmarks
- Off-line simulations
 - Various hardware
- On-line profiling
 - $\alpha = 0.5$, then update
 - Statistical machine learning



Fitting Utilities



•
$$u = \prod_{r=1}^{R} x_r^{\alpha_r}$$



Fitting Utilities



• $u = \prod_{r=1}^{R} x_r^{\alpha_r}$

•
$$\log(u) = \sum \alpha_r \log(x_r)$$



Fitting Utilities



- $u = \prod_{r=1}^{R} x_r^{\alpha_r}$
- $\log(u) = \sum \alpha_r \log(x_r)$
- Use linear regression to find α_r



Cobb-Douglas Accuracy

- IPC as utility
- Cache size and memory bandwidth
- R-squared \rightarrow 1 as fit improves





Cobb-Douglas Accuracy

- IPC as utility
- Cache size and memory bandwidth
- R-squared \rightarrow 1 as fit improves



Ferret Sim. + Ferret Est.





• Normalize elasticities to sum to one





• Normalize elasticities to sum to one

•
$$u = x^{0.2}y^{0.3}$$





• Normalize elasticities to sum to one

•
$$u = x^{0.2}y^{0.3} \to u = x^{0.4}y^{0.6}$$





• Normalize elasticities to sum to one

•
$$u = x^{0.2}y^{0.3} \to u = x^{0.4}y^{0.6}$$

• Compare users' elasticities on same scale



Allocating Proportional Shares



• Use elasticities as weights



Allocating Proportional Shares



- Use elasticities as weights
- Share proportionally


Allocating Proportional Shares



- Use elasticities as weights
- Share proportionally
 - E.g., lottery scheduling



Allocating Proportional Shares



- Use elasticities as weights
- Share proportionally
 - E.g., lottery scheduling
 - E.g., weighted fair queuing



Example Allocations

$$\mathsf{u}_1 = \mathsf{x}_1^{0.6} \mathsf{y}_1^{0.4} \qquad \mathsf{u}_2 = \mathsf{x}_2^{0.2} \mathsf{y}_2^{0.8}$$



Example Allocations

$$\begin{split} \textbf{u}_1 &= \textbf{x}_1^{0.6} \textbf{y}_1^{0.4} \qquad \textbf{u}_2 &= \textbf{x}_2^{0.2} \textbf{y}_2^{0.8} \\ \textbf{x}_1 &= \left(\frac{0.6}{0.6+0.2}\right) \times 24 = 18 \text{GB/s} \end{split}$$



Example Allocations

$$\begin{split} \textbf{u}_1 &= \textbf{x}_1^{0.6} \textbf{y}_1^{0.4} \qquad \textbf{u}_2 &= \textbf{x}_2^{0.2} \textbf{y}_2^{0.8} \\ \textbf{x}_1 &= \left(\frac{0.6}{0.6+0.2}\right) \times 24 = 18 \text{GB/s} \\ \textbf{x}_2 &= \left(\frac{0.2}{0.6+0.2}\right) \times 24 = 6 \text{GB/s} \end{split}$$



• Simulators

• MARSSx86 for processors



- MARSSx86 for processors
- DRAMSim2 for memory



- MARSSx86 for processors
- DRAMSim2 for memory
- Benchmarks



- MARSSx86 for processors
- DRAMSim2 for memory
- Benchmarks
 - PARSEC



- MARSSx86 for processors
- DRAMSim2 for memory
- Benchmarks
 - PARSEC
 - SPLASH-2x



- MARSSx86 for processors
- DRAMSim2 for memory
- Benchmarks
 - PARSEC
 - SPLASH-2x
 - Phoenix MapReduce



Fairness versus Equal Slowdown



- Equal slow-down provides neither SI nor EF
- Canneal receives < half of cache, memory



Fairness versus Equal Slowdown



- Equal slow-down provides neither SI nor EF
- Canneal receives < half of cache, memory



- Resource elasticity fairness provides both SI and EF
- Canneal receives more cache, less memory



Fairness versus Performance



• Measure weighted throughput



Fairness versus Performance



- Measure weighted throughput
- REF incurs < 10% penalty



• Model performance with Cobb-Douglas utility



- Model performance with Cobb-Douglas utility
 - DMR, substitution effects



- Model performance with Cobb-Douglas utility
 - DMR, substitution effects
- Guarantee fairness with REF



- Model performance with Cobb-Douglas utility
 - DMR, substitution effects
- Guarantee fairness with REF
 - SI, EF, PE, SPL



- Model performance with Cobb-Douglas utility
 - DMR, substitution effects
- Guarantee fairness with REF
 - SI, EF, PE, SPL
- Apply to chip multiprocessors



- Model performance with Cobb-Douglas utility
 - DMR, substitution effects
- Guarantee fairness with REF
 - SI, EF, PE, SPL
- Apply to chip multiprocessors
 - Cache size, memory bandwidth



- Model performance with Cobb-Douglas utility
 - DMR, substitution effects
- Guarantee fairness with REF
 - SI, EF, PE, SPL
- Apply to chip multiprocessors
 - Cache size, memory bandwidth
- Incur small performance penalty



- Model performance with Cobb-Douglas utility
 - DMR, substitution effects
- Guarantee fairness with REF
 - SI, EF, PE, SPL
- Apply to chip multiprocessors
 - Cache size, memory bandwidth
- Incur small performance penalty
 - $\bullet\ <$ 10% throughput loss



Thank you

Questions?



28 / 28